

28 Apr 2011
At NCL Innovation
Park

Workshop on

Digital Design/ Prototyping with Verilog

By Mr. Basu (B.Tech,M. Tech from IIT,KGP; ex-Intel)



For whom

- Aspiring Entrepreneurs
- Digital Design learning aspirants with background in digital Circuits Theory and Boolean Logic
- Electronic/Robotics hobbyists who aspire to design using FPGA's
- PG students /Final year UD students in electronics/ Instrumentation engineering

Venue & Timings

Date: Thursday, 28 Apr 2011

Time: 9 am – 6 pm

Address :

E-class room, Venture Center,
100, NCL Innovation Park,
Dr.Homi Bhabha Road,
Pune 411008

E-Mail:

software@venturecenter.co.in

Phone: 020-2590-2984

Fee: RS 1000 per person

Discount: 25% if you bring your own laptop

Further, 25% discount for student against valid ID cards

Workshop includes

- Classroom and practical sessions about digital designs and get practical insights
- 150 page handout with useful, practical information
- Access to restricted website with links to useful resources, software resources etc
- Joining a mailing list community of digital design enthusiasts
- Certificate of Participation from Venture Center
- Basic lunch and Tea/Coffee